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ROSSI

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08/25/2003 TTRAN2 00000069 182056 10020573

01 FC:1501 1300.00 DA
02 FC:1504 300.00 DA
03 FC:8001 12.00 DA

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/020,373	10/31/2001	Takashi Kobayashi	FUJI200	5661

TITLE OF INVENTION: VERTICAL INSULATED GATE SEMICONDUCTOR DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1300	\$300	\$1600	09/04/2003

EXAMINER	ART UNIT	CLASS-SUBCLASS
GREENE, PERSHELLE L	2826	257-135000

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FUJI ELECTRIC CO., LTD.

JAPAN

(B) RESIDENCE: (CITY AND STATE OR COUNTRY)

Please check the appropriate assignee category or categories (will not be printed on the patent)

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Marc A. Rossi, Reg 31,923

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08/22/03

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